



A cross-sectional view of a semiconductor device. A central channel region (80) is shown with a dashed line indicating a boundary or interface. The channel is flanked by two side regions (84) which are shaded with diagonal lines. The top surface of the device is indicated by a dashed line labeled '0'. The bottom surface is indicated by a solid line labeled '+'. The channel region (80) is labeled with '82' and '86'. The side regions (84) are labeled with '84' and '86'.

Fig. 2B

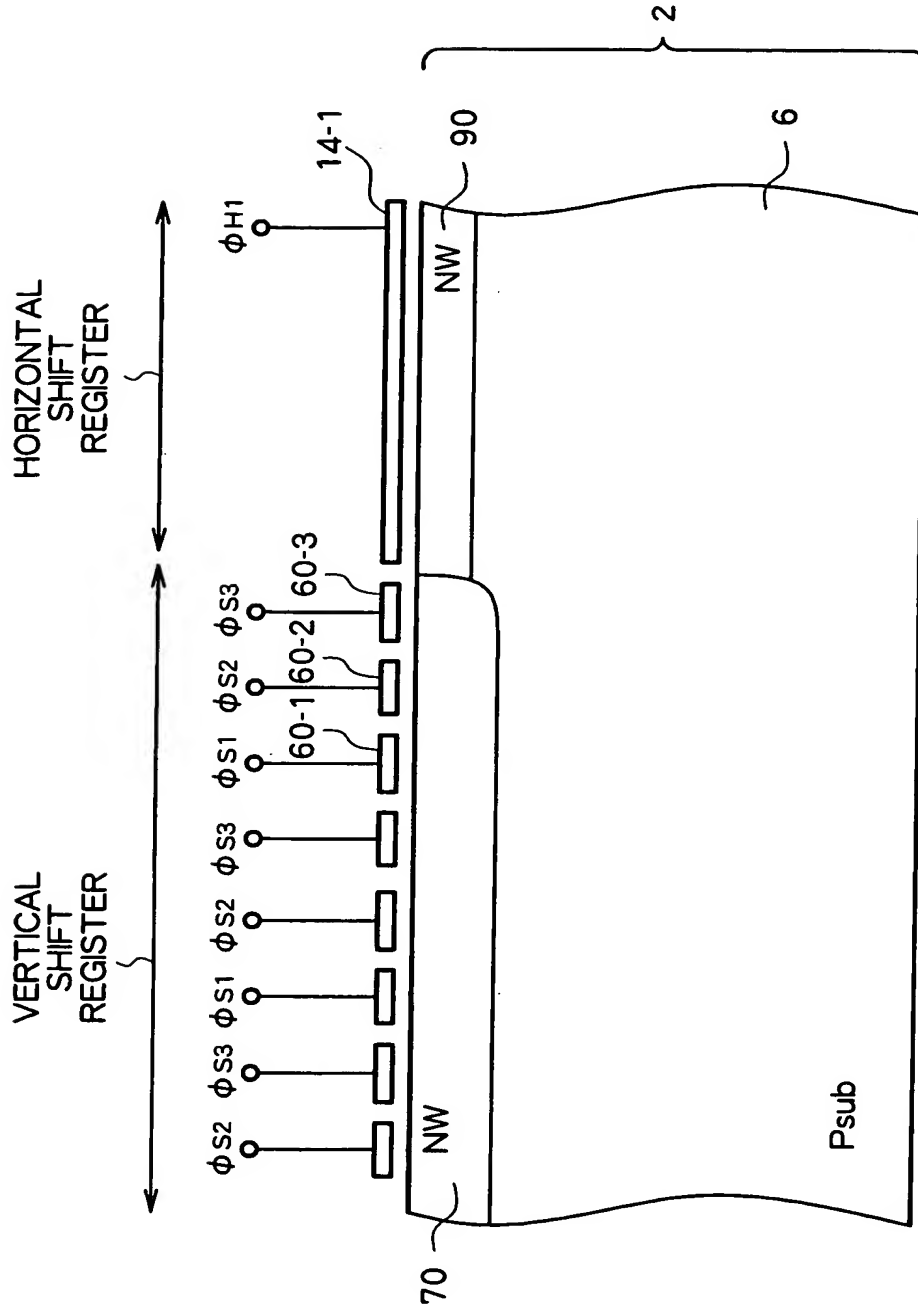
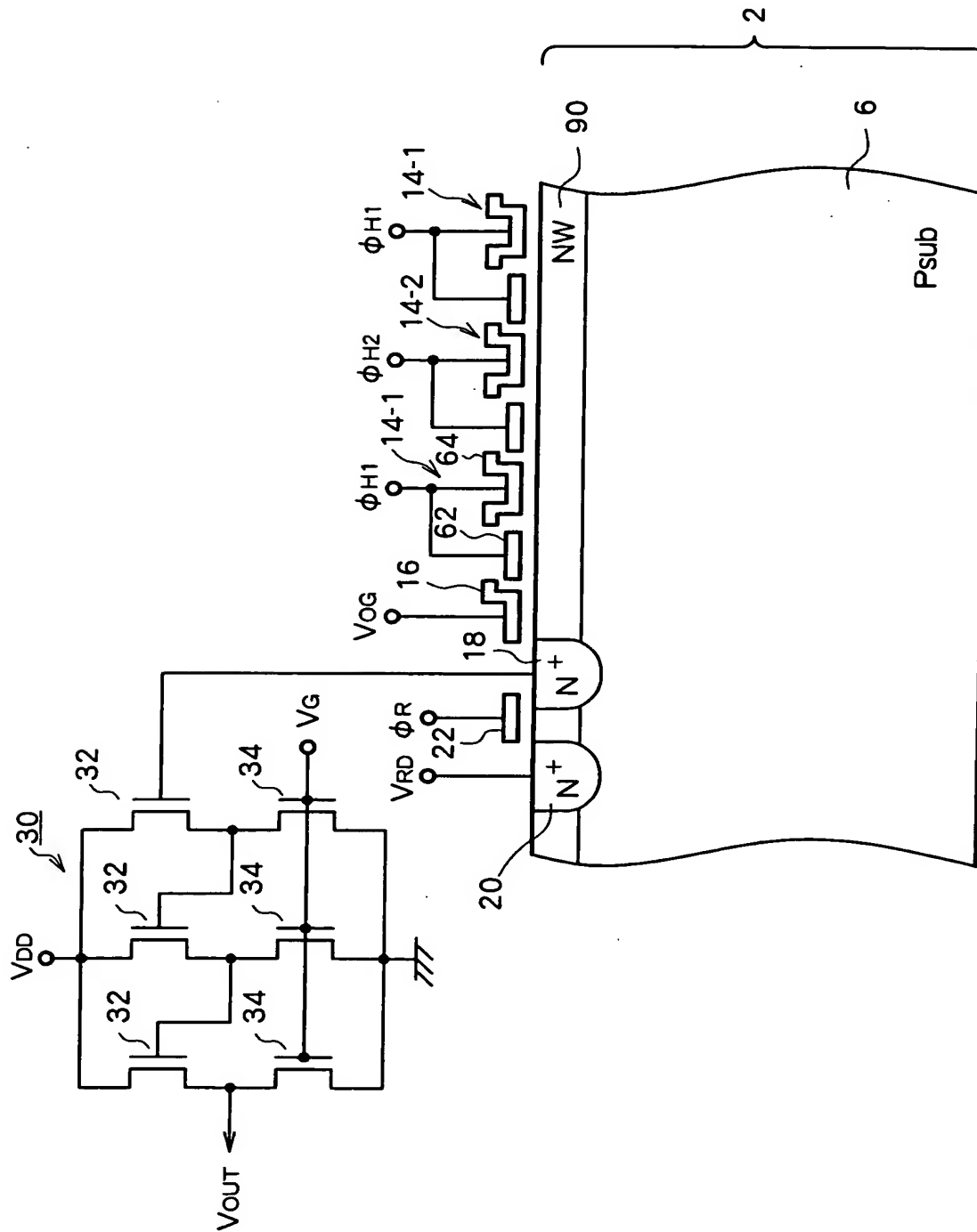


Fig. 3



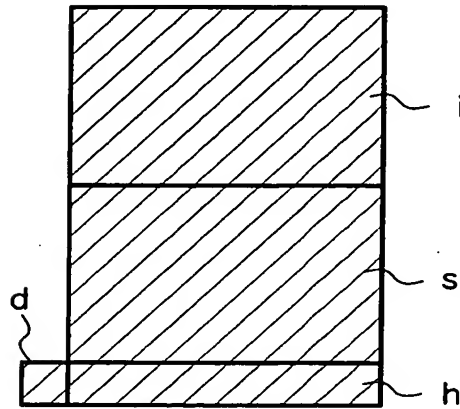


Fig. 5A

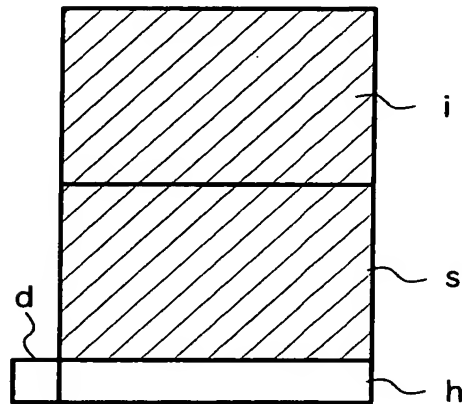


Fig. 5B

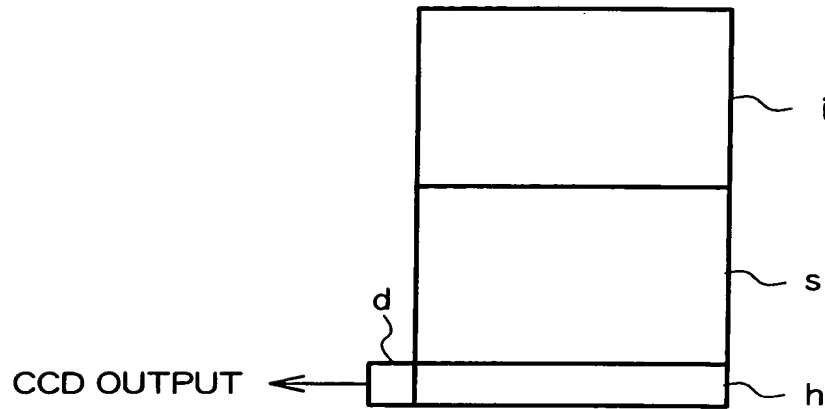


Fig. 6

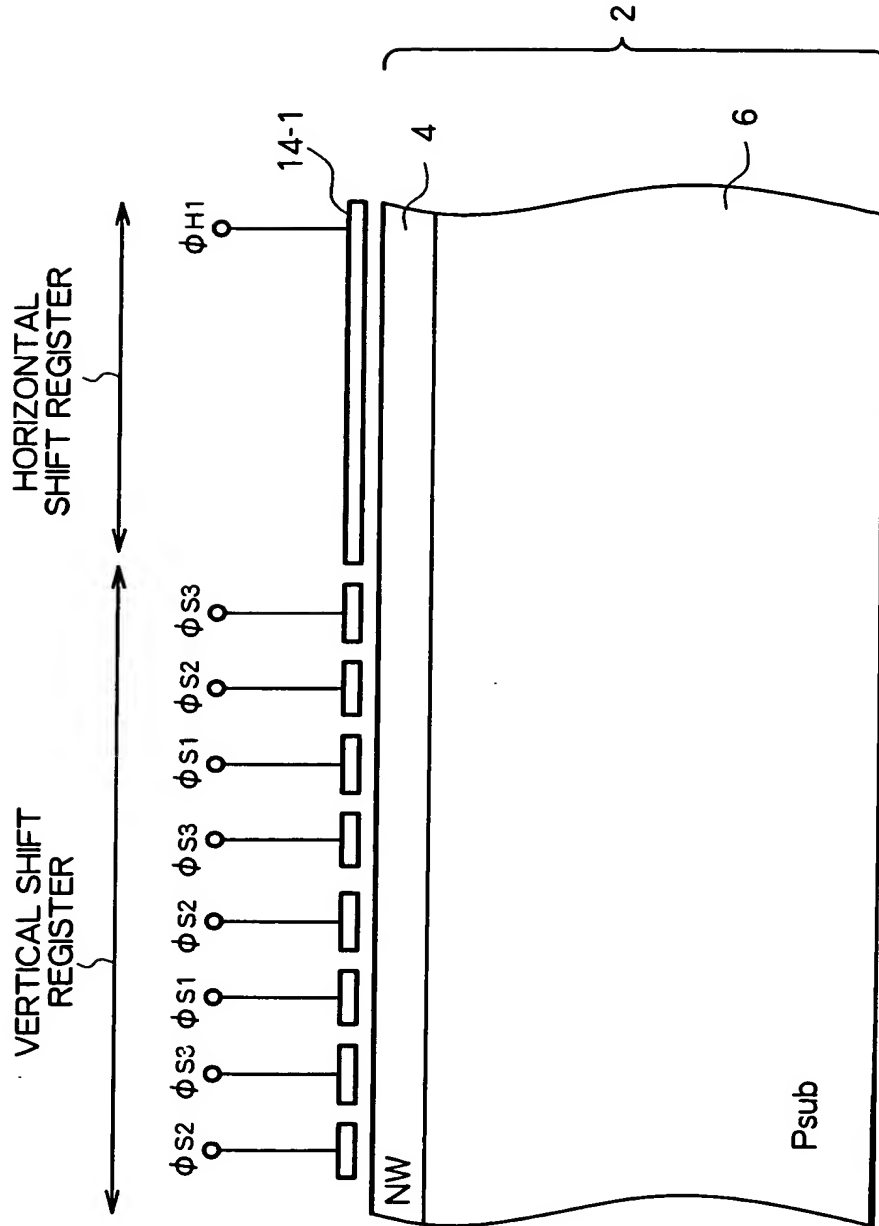
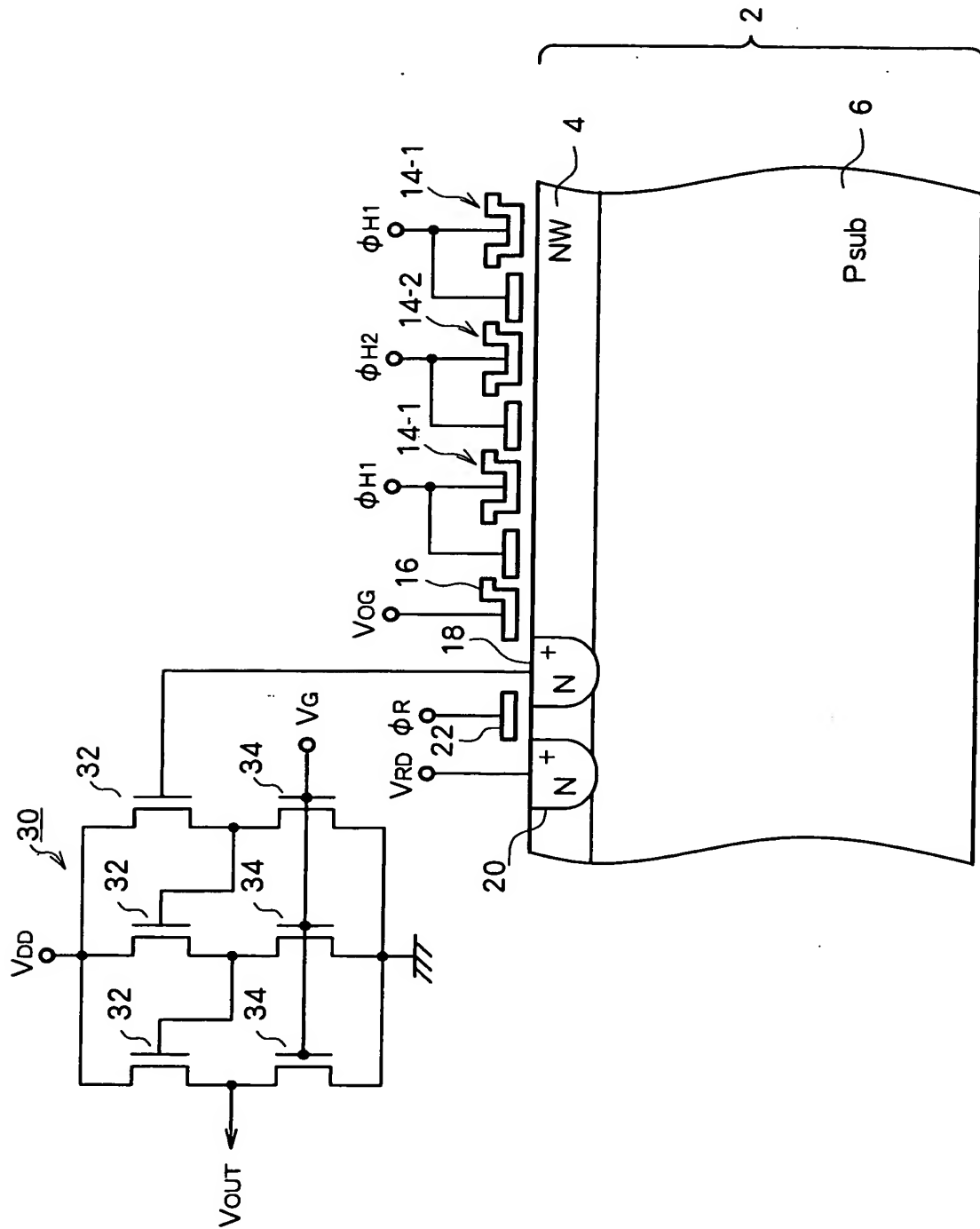


Fig. 7



∞
ω
i